Microprocessor Supervisory Circuit

FEATURES

- *Guaranteed* Reset Assertion at V_{CC} = 1V
- 1.5mA Maximum Supply Current
- SO-8 Packaging
- 4.65V Precision Voltage Monitor
- Power OK/Reset Time Delay: 200ms
- Minimum External Component Count
- Performance Specified Over Temperature
- Superior Upgrade for MAX699

APPLICATIONS

- Critical µP Power Monitoring
- Intelligent Instruments
- Computers and Controllers
- Automotive Systems

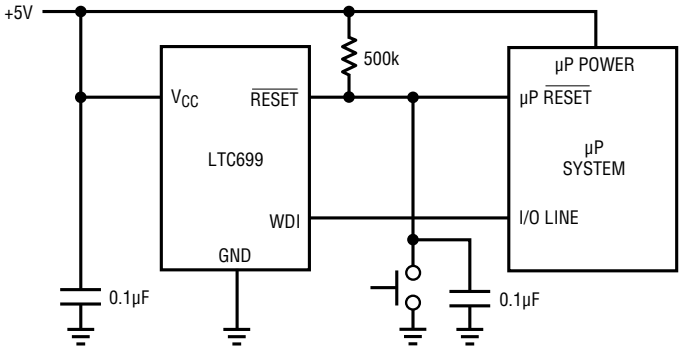
DESCRIPTION

The LTC699 provides power supply monitoring for microprocessor-based systems. The features include microprocessor reset and watchdog timing. Precise internal voltage reference and comparator circuit monitor the power supply line. When an out-of-tolerance condition occurs, the $\overline{\text{RESET}}$ output is forced to active low. In addition, the $\overline{\text{RESET}}$ output is guaranteed to remain logic low even with V_{CC} as low as 1V.

An internal watchdog timer is also available, which forces the $\overline{\text{RESET}}$ output to active low when the watchdog input is not toggled prior to the time-out period of 1.6 seconds.

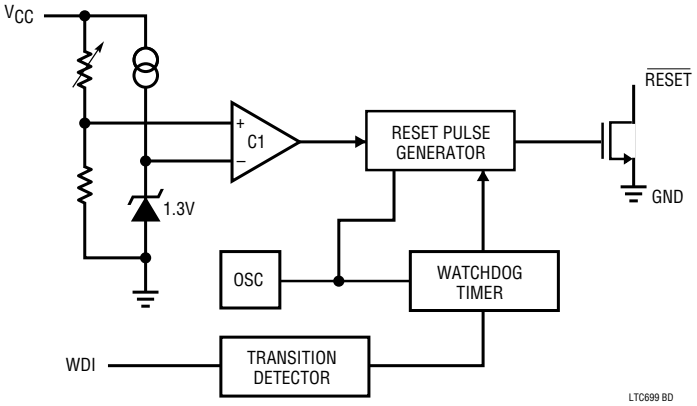
The LTC699 is offered in DIP and surface mount packages.

TYPICAL APPLICATION



MICROPROCESSOR RESET AND WATCHDOG TIMING IN A SINGLE CHIP FOR MICROPROCESSOR SYSTEMS.

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

(Notes 1 and 2)

Terminal Voltage
- V_{CC} ..... −0.3V to 6.0V
- WDI Input ..... −0.3V to V_{CC} + 0.3V
- $\overline{RESET}$ Output ..... −0.3V to 6V

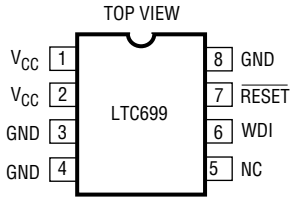
Power Dissipation 500mW

Operating Temperature Range
- LTC699C ..... 0°C to 70°C
- LTC699I ..... −40°C to 85°C

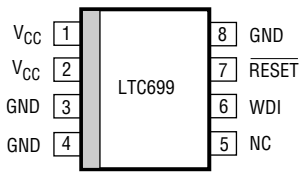
Storage Temperature Range ..... −65°C to 150°C

Lead Temperature (Soldering, 10 sec.) ..... 300°C

PACKAGE/ORDER INFORMATION



N8 PACKAGE
8-LEAD PLASTIC DIP



S8 PACKAGE
8-LEAD PLASTIC SOIC

ORDER PART NUMBER
LTC699CN8 LTC699IN8
LTC699CS8 LTC699IS8

ELECTRICAL CHARACTERISTICS

V_{CC} = +5V, T_A = 25°C, unless otherwise noted.

PARAMETER	CONDITONS		MIN	TYP	MAX	UNITS
Operating Voltage Range		●	3.0		5.5	V
Supply Current		 ●		0.6 0.6	1.5 2.5	mA
Power Down Reset Assertion		●	4.5	4.65	4.75	V
Power Up Reset De-Assertion		●			4.75	V
Reset Threshold Hysteresis				40		mV
Reset Active Time		 ●	160 140	200 200	240 280	ms
Watchdog Time-out Period		 ●	1.2 1.0	1.6 1.6	2.0 2.25	sec
Reset Active Time PSRR				1		ms/V
Watchdog Time-out Period PSRR				8		ms/V
Minimum WDI Input Pulse Width	V_{IL} = 0.4, V V_{IH} = 3.5V	●	200			ns
$\overline{RESET}$ output Voltage at V_{CC} = 1V (Note 3)	I_{SINK} = 10µA, V_{CC} = 1V			4	200	mV
$\overline{RESET}$ output Voltage	I_{SINK} = 1.6mA, V_{CC} = 4.25V				0.4	V
$\overline{RESET}$ Output Short Circuit Current	Output Sink Current			25		mA
WDI Input Threshold	Logic Low Logic High	 	 2.0		0.8	V
WDI Input Current	WDI = V_{OUT} WDI = 0V	● ●	 –50	4 –8	50	µA

The ● denotes specifications which apply over the operating temperature range.

Note 1: Absolute maximum ratings are those values beyond which the life of device may be impaired.

Note 2: All voltage values are with respect to GND.

Note 3: $\overline{RESET}$ is active low, open drain output.

PIN FUNCTIONS

V_{CC}: +5V supply input. The V_{CC} pin should be bypassed with a 0.1μF capacitor.

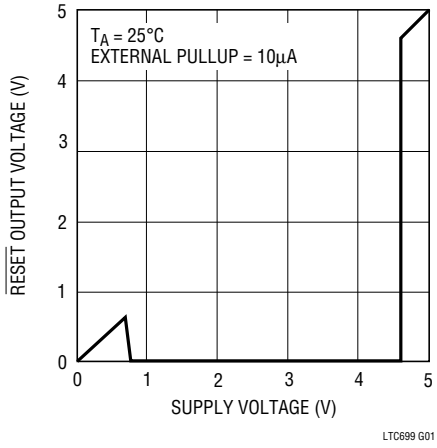
GND: Ground pin.

$\overline{RESET}$: Open drain output for μP reset control. When V_{CC} falls below the reset voltage threshold (4.65V typically), $\overline{RESET}$ goes active low. After V_{CC} returns to 5V, the reset pulse generator forces $\overline{RESET}$ to remain active low for a minimum of 140ms . When the watchdog timer is enabled but not serviced prior to the time-out period, the reset pulse generator also forces $\overline{RESET}$ to active low for a minimum of 140ms for every time-out period (see Figure 2).

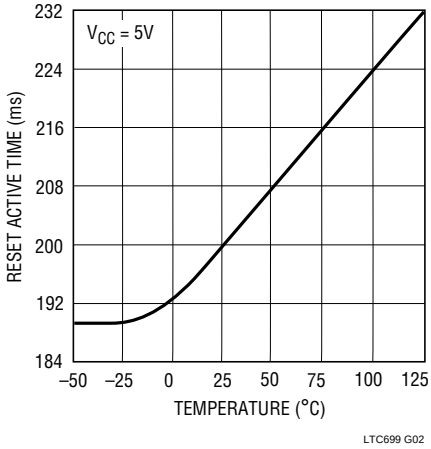
WDI: Watchdog Input, WDI, is a three level input. Driving WDI either high or low for longer than the watchdog time-out period forces $\overline{RESET}$ low. Floating WDI disables the Watchdog Timer. The timer resets itself with each transition of the Watchdog Input (see Figure 2).

TYPICAL PERFORMANCE CHARACTERISTICS

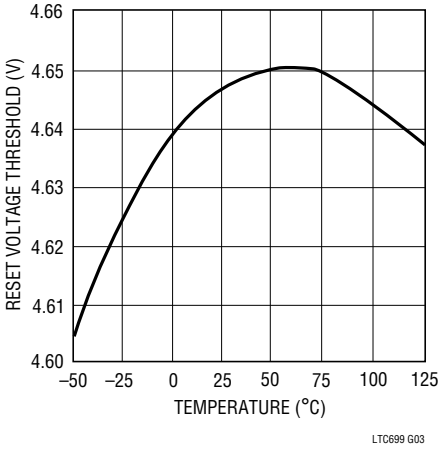
$\overline{RESET}$ Output Voltage vs Supply Voltage



Reset Active Time vs Temperature



Reset Voltage Threshold vs Temperature



APPLICATIONS INFORMATION

Microprocessor Reset

The LTC699 uses a bandgap voltage reference and a precision voltage comparator C1 to monitor the 5V supply input V_{CC} (see BLOCK DIAGRAM). When V_{CC} falls below the reset voltage threshold, the $\overline{RESET}$ output is forced to active low state. The reset voltage threshold accounts for a 5% variation on V_{CC}, so the $\overline{RESET}$ output becomes active low when V_{CC} falls below 4.65V typical. On power-up, the $\overline{RESET}$ signal is held active low for a minimum of 140ms after reset voltage threshold is reached to allow the power supply and microprocessor to stabilize. On power-down, the $\overline{RESET}$ signal remains active low even with V_{CC} as low as 1V. This capability helps hold the microprocessor in stable shutdown condition. Figure 1 shows the timing diagram of the $\overline{RESET}$ signal.

The precision voltage comparator, C1, typically has 40mV of hysteresis which ensures that glitches at V_{CC} pin do not activate the $\overline{RESET}$ output. Response time is typically 10μs. To help prevent mistriggering due to transient loads, V_{CC} pin should be bypassed with a 0.1μF capacitor with the leads trimmed as short as possible.

APPLICATIONS INFORMATION

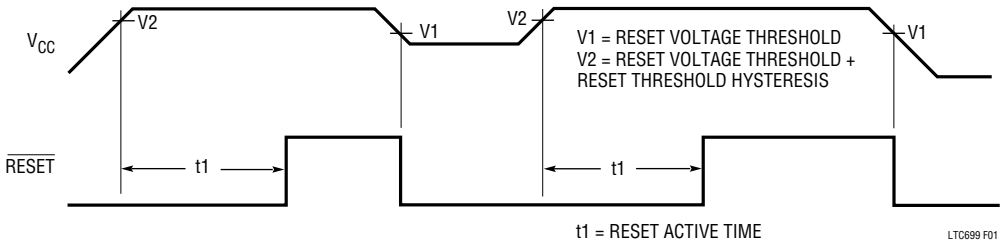


Figure 1. Reset Active Time

Watchdog Timer

The LTC699 provides a watchdog timer function to monitor the activity of the microprocessor. If the microprocessor does not toggle the Watchdog Input (WDI) within the time-out period, $\overline{RESET}$ is forced to active low for a minimum of 140ms. The watchdog time-out period is fixed at a 1.0 second minimum on the LTC699, which is adequate time for most systems to service the watchdog timer immediately after a reset. Figure 2 shows the timing diagram of watchdog time-out period and reset active time. The watchdog time-out period is restarted as soon as $\overline{RESET}$ is inactive. When either a high-to-low or low-to-high transition occurs at the WDI pin prior to time-out, the watchdog time is reset and begins to time out again. To ensure the watchdog time does not time out, either a high-to-low or low-to-high transition on the WDI pin must occur at or less than the minimum time-out period. If the input to the WDI pin remains either high or low, reset pulses will be issued every 1.6 seconds typically. The watchdog time can be deactivated by floating the WDI pin. The timer is also disabled when V_{CC} falls below the reset voltage threshold.

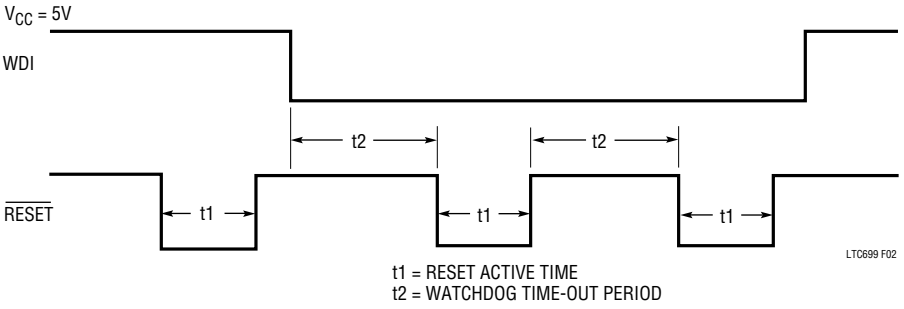
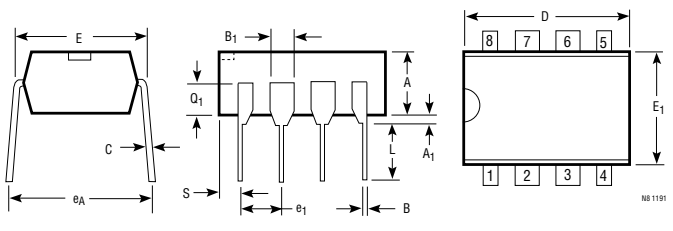


Figure 2. Watchdog Time-Out Period and Reset Active Time

PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

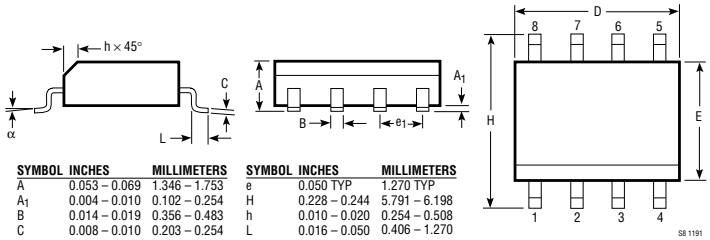
N8 Package
8-Lead Plastic DIP



SYMBOL	INCHES	MILLIMETERS	SYMBOL	INCHES	MILLIMETERS
A	0.130 ± 0.006	3.302 ± 0.127	E_1	0.250 ± 0.010	6.350 ± 0.254
A_1	0.020 MIN	0.508 MIN	e_A	0.325 + 0.025	8.255 + 0.635
B	0.018 ± 0.003	0.457 ± 0.076		–0.015	–0.381
B_1	0.045 – 0.065	1.143 – 1.651	e_1	0.100 ± 0.010	2.540 ± 0.254
C	0.009 – 0.015	0.229 – 0.381	L	0.125 MIN	3.175 MIN
D	0.400 MAX	10.160 MAX	Q_1	0.065 TYP	1.651 TYP
E	0.300 – 0.320	7.620 – 8.128	S	0.045 ± 0.015	1.143 ± 0.381

T_{JMAX}	θ_{JA}
110°C	130°C/W

S8 Package
8-Lead Plastic SOIC



SYMBOL	INCHES	MILLIMETERS	SYMBOL	INCHES	MILLIMETERS
A	0.053 – 0.069	1.346 – 1.753	e	0.050 TYP	1.270 TYP
A_1	0.004 – 0.010	0.102 – 0.254	H	0.228 – 0.244	5.791 – 6.198
B	0.014 – 0.019	0.356 – 0.483	h	0.010 – 0.020	0.254 – 0.508
C	0.008 – 0.010	0.203 – 0.254	L	0.016 – 0.050	0.406 – 1.270
D	0.189 – 0.197	4.801 – 5.004	α	0° – 8° TYP	
E	0.150 – 0.157	3.810 – 3.988			

T_{JMAX}	θ_{JA}
110°C	180°C/W

CONDITIONS: PCB MOUNT ON FR4 MATERIAL, STILL AIR AT 25°C, COPPER TRACE

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